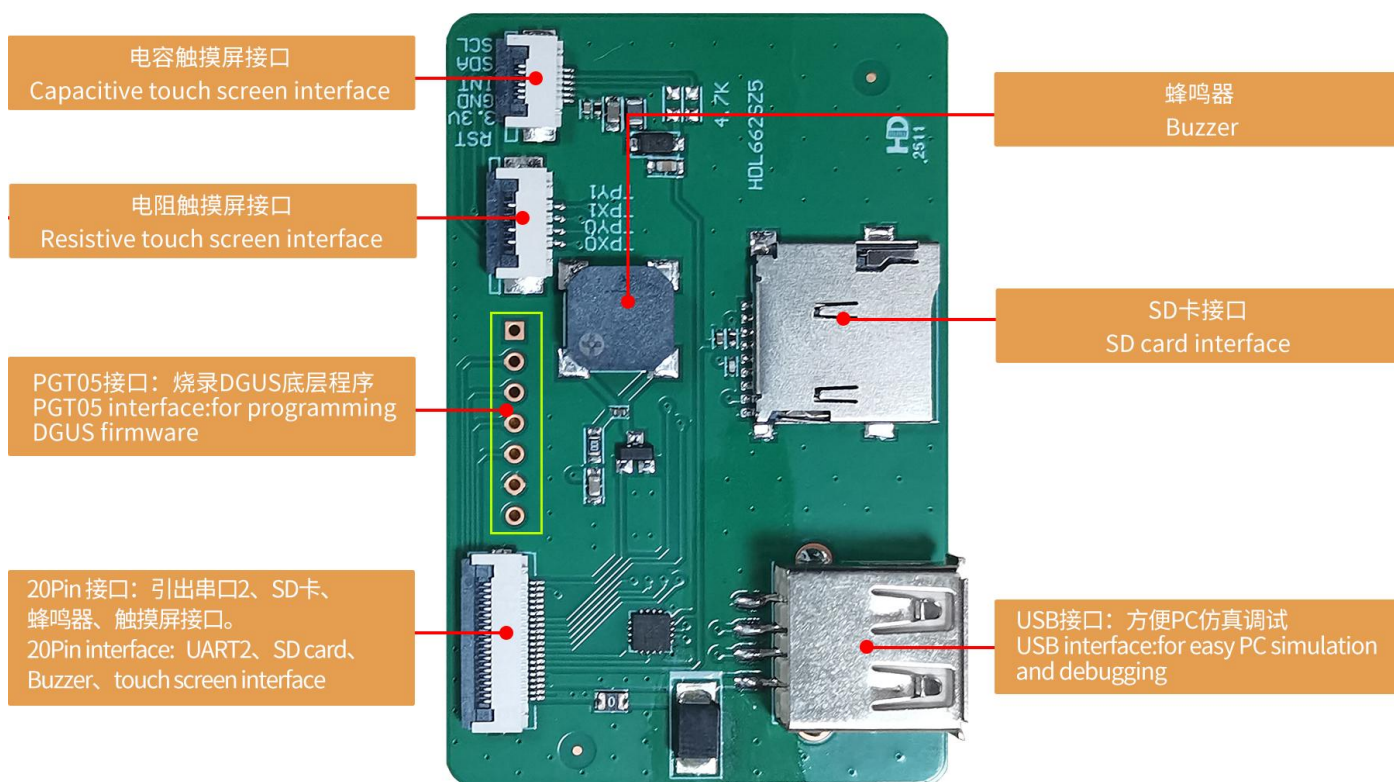


HDL662SZ5



1、硬件及接口 Hardware and interface

1.1 硬件接口图 Hardware interface diagram



硬件接口图
Hardware interface diagram

1.2 硬件及接口说明 Hardware and interface description

序号 No.	项目 Item	说明 Description
1	USB 接口 Use interface	用于供电和 PC 仿真调试。 For power supply and easy PC simulation and debugging.
2	20Pin 接口 20Pin interface	连接 FCC20_0.5 L=50mm, 引出串口 2、SD 卡、蜂鸣器、触摸屏接口。 For connecting FCC20_0.5 L=50mm, lead out UART2、SD card、buzzer、touch screen interface.
3	电容触摸屏接口 Capacitive touch screen interface	用于连接电容触摸屏。 For connecting capacitive touch screen.
4	电阻触摸屏接口 Resistive touch screen interface	用于连接 4 线电阻触摸屏。 For connecting 4-wire resistive touch screen.
5	蜂鸣器 Buzzer	3V 无源蜂鸣器。 3V passive buzzer.
6	SD 卡接口 SD card interface	用于 DGUS 工程文件下载 (UI 文件、CFG 文件、内核等), 下载速率: 4Mb/s。 For DGUS project file downloads (UI, CFG files, kernel, etc.), 4 Mb/s rate.
7	PGT05 接口 PGT05 interface	用于烧录 DGUS 底层程序。 For programming DGUS firmware.

附：20Pin 接口说明 20Pin interface description

PIN# 序号	Definition1 定义 1	Function1 功能 1	Definition2 定义 2	Function2 功能 2
1	VIN	供电输入，DC3.3-5.5V。IO 均为 3.3V CMOS 电平。 Power supply, DC3.3-5.5V. IO are all at 3.3V CMOS level.		
2	VIN			
3	GND	GND		
4	GND			
5	GND			
6	3.3V OUT	3.3V 输出，最大负载 100mA。 3.3V output, maximum load of 100mA.		
7	TX2	UART2		
8	RX2			
9	SD_D0	JIOS 浮空时，SD 卡接口。 When JIOS is in the air, SD card interface.	TMS	JIOS 对地短路时，JTAG 接口。 When JIOS is shorting circuit to ground, JTAG interface.
10	SD_D1		TCK	
11	SD_D2		TDI	
12	SD_D3		TDO	
13	SD_CLK		TX1	复用的 UART1 Multiplexed UART1
14	SD_CMD		RX1	
15	TPY1	4 线电阻触摸屏接口 4-wire resistive touch screen interface	CIP_SDA	电容触摸屏接口 接 CTP 时，CIP_SDA、CTP_INT 需外部 4.7K 上拉到 3.3V。 Capacitive touch screen interface When connected to CTP, the CIP_SDA and CTP_INT need to be pulled up to 3.3V by an external 4.7K resistance.
16	TPX1		CTP_INT	
17	TPY0		CTP_SCL	
18	TPX0		CTP_RST	
19	JIOS	JTAG/IO 选择 JTAG/IO choose		
20	BUZZ	蜂鸣器驱动输出 Buzzer drives output	D/A	音频 PWM DA 输出 Audio PWM DA output

2、规格参数 Specification parameters

2.1 串口参数 Serial interface parameters

串口模式 Mode	UART2:TTL				
串口电平 Voltage Level	测试条件 Test Condition	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
	Output 1	3.0	3.3	-	V
	Output 0	-	0	0.3	V
	Input 1	-	-	3.3	V
	Input 0	0	-	0.5	V
串口波特率 Baud Rate	3150~3225600bps, 典型值: 115200bps。 3150~3225600bps, typical value of 115200bps.				
数据格式 Data Format	UART2: N81				
接口端子 Interface Cable	20Pin_0.5mm				

2.2 电气规格 Electrical specifications

额定功率 Rated Power	<5W
工作电压 Operating Voltage	3.3~5.5V, 典型值 5V。 3.3~5.5V, typical value of 5V.

2.3 工作环境 Operating environment

工作温度 Operating Temperature	-10℃~60℃ (5V @ 60% RH)
存储温度 Storage Temperature	-20℃~70℃
防紫外线 Anti-UV	无 N
三防漆 Conformal coating	无 N
工作湿度 Operating Humidity	10%~90%RH, 典型值 60%RH。 10%~90%RH, typical value of 60% RH.

3、包装和物理尺寸 **Packaging & dimensions**

外形尺寸 Form Factor	63.90mm (W)×40.60mm (H) ×9.40mm (T)
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4、修订记录 Revision records

版本 Rev	日期 Revise Date	描述 Content	编辑人 Editor
00	2025-06-03	首次发布 First Edition	赵方维

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