# **DMG10600T070\_A5WTR**

#### Features:

- Based on T5L2, running DGUS II system, industrial grade.
- 7.0-inch, 1024\*600 pixels resolution, 16.7M Colors, IPS-TFT-LCD, wide viewing angle.
- 5-wire resistance touch panel.
- With shell, with conformal coating.



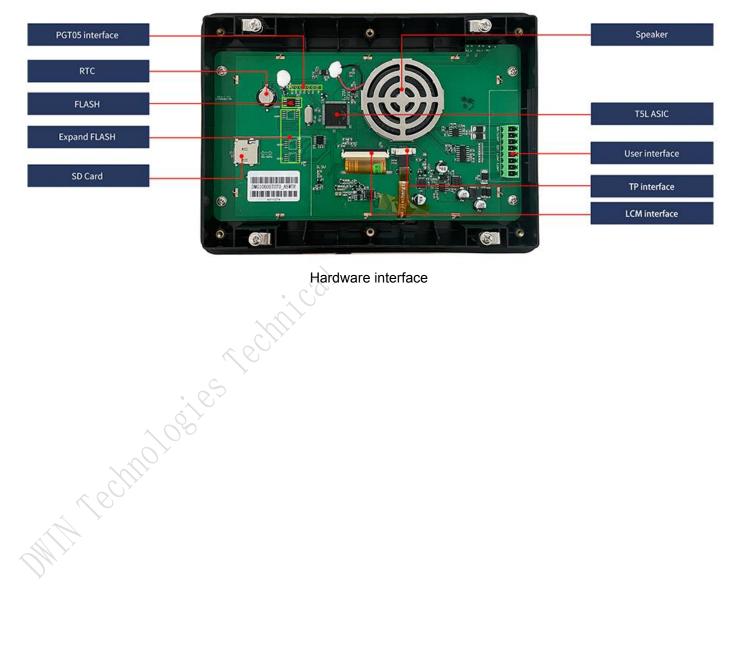






#### 1 Hardware and interface

#### 1.1 Hardware interface



Hardware interface



#### 1.2 Interface description



# 2 Specification parameters

#### 2.1 Display parameters

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LCD Type	IPS, TFT LCD			
Viewing Angle	Wide viewing angle, 85°/85°/85° (L/R/U/D)			
Resolution	1024×600 pixels (support 0°/90°/180°/270°)			
Color	24-bit 8R8G8B			
Active Area (A.A.)	154.21mm (W) ×85.9mm (H)			
Visual Area (V.A.)	155.1mm (W) ×86.9mm (H)			
Backlight	LED			
Backlight Lifetime	>30000 hours (Time of the brightness decaying to 50% on the condition of continuous working with the maximum brightness)			
Brightness	250nit			
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)			
Note: Long time display of high contract still image ever 20 minutes may lead to display residual				

Note: Long time display of high contrast still image over 30 minutes may lead to display residual shadow, please use screen saver to avoid this problem.

# 2.2 Touch parameters

Туре	Five-wire RTP (Resistive touch panel)			
Structure	ITO film + ITO glass			
Touch Mode	Support point touch and drag			
Surface Hardness	3H			
Light Transmittance	Over 80%			
Life	Over 1,000,000 times touch			

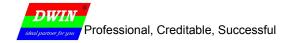


### 2.3 Serial interface parameters

	UART2: RS232				
Mode	de UART4: RS485(Only available after OS configuration)				
	UART5: RS232(Only availal	ole after OS co	nfiguration)		
	Test Condition	Min	Тур	Max	Unit
	Output 1	-	-5.0	-3.0	V
Voltage Level (UART2, UART5)	Output 0	3.0	5.0	-	V
,	Input 1	-15.0	-5.0	-	V
	Input 0	-	5.0	15.0	V
Baud Rate (UART2, UART5)	3150~3225600bps, typical value of 115200bps				
	Test Condition	Min	Тур	Max	Unit
	Output 1	2.5	5.0	-	V
Voltage Level (UART4)	Output 0	-	-0.5	-2.5	V
(0	Input 1	0	2.5	-	V
	Input 0	-	-2.5	-0.2	V
Baud Rate (UART4)	3150~3225600bps, typical value of 115200bps				
	UART2: N81				
Data Format	UART4: N81/E81/O81/N82 4 modes (OS configuration)				
	UART5: N81/E81/O81/N82 4 modes (OS configuration)				
Interface Cable	8Pin_3.81mm Socket				

### 2.4 Electrical specifications

Rated Power	<5W		
Operating Voltage	7~36V, typical value of 12V		
Operating Current	250mA	VCC=12V, max backlight	
	80mA	VCC=12V, backlight off	
Recommended power supply: 12V 1A DC			



#### 2.5 Operating environment

Operating Temperature	-20℃~70℃ (12V @ 60% RH)		
Storage Temperature	-30℃~80℃		
Conformal coating	Yes		
Operating Humidity	10%~90%RH, typical value of 60% RH		
Protective Level	IP65 (Front)		

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# 3 Reliability test

#### 3.1 Electrostatic discharge test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and air discharge in turn of the serial screen iron frame and display area as shown in Fig.3.1 below. During the experimental process, it was observed whether the screen is dead, black, white, splash, or reboot. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.



3.1 Electrostatic discharge test

Discharge Type	Discharge Value	Result
Contact discharge	±6KV	Normal operation
Air discharge	±8KV	Normal operation
DINIT RECITION		

#### 3.2 EFT test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and the smart screen is energized by the power supply coupled with a EFT generator as shown in Fig. 3.2 below. During the experimental process, it was observed whether abnormal reset, display or touch phenomena occurs. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.



3.2 EFT test

Test Item	Test Standard	Result
Power supply	±2KV;100KHz	Normal operation
	A 0C)	
	Q7	
10°2	<b>Y</b>	
Chr		
OM.		
7		



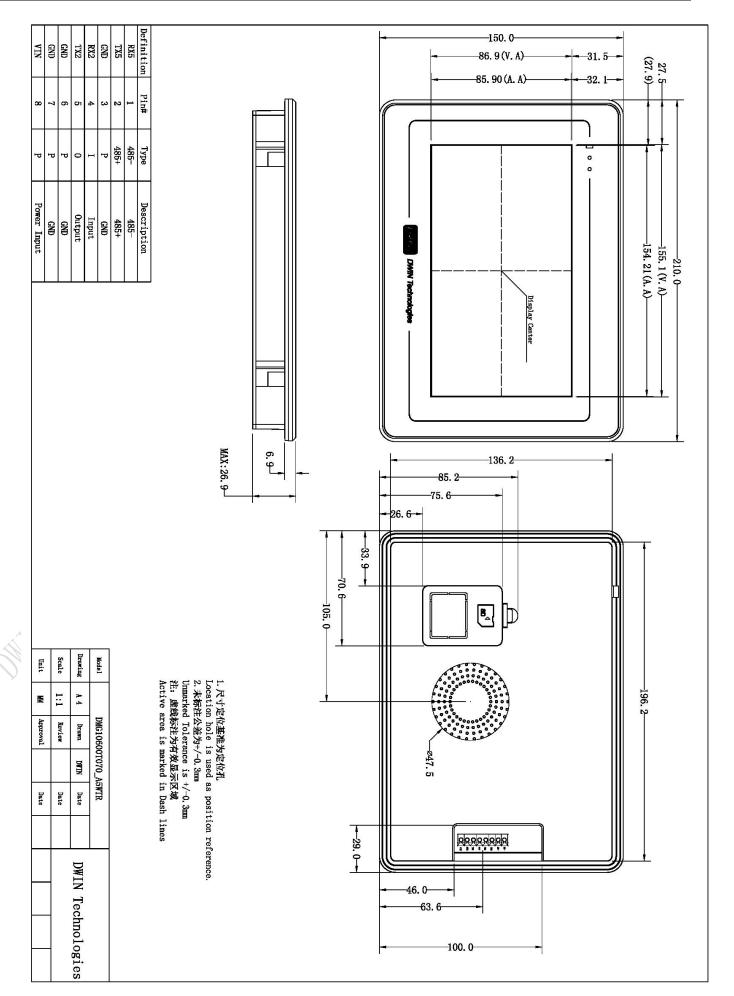
# 4 Packaging & dimensions

Form Factor	210.0mm(W) ×150.0mm(H)×26.9mm(T)		
Installation Dimensions	Positioning hole: 196.2(+0.5mm)×136.2(+0.5mm)		
Net Weight	500g		

### Packaging Standards

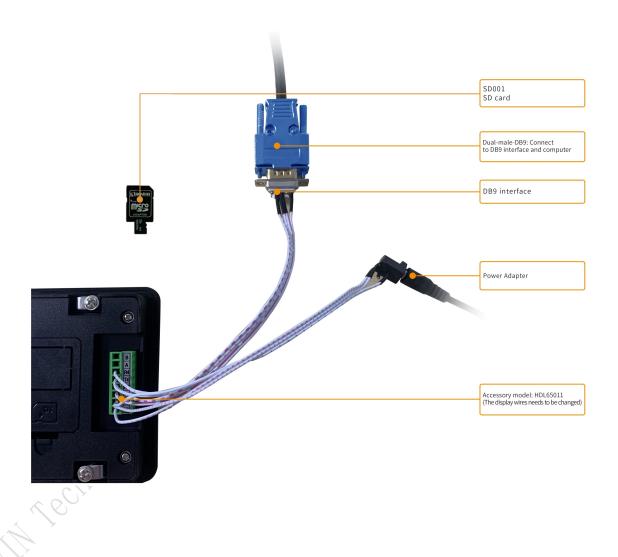
Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)
Carton1:	220mm(L)×160mm(W)×47mm (H)	-	-	-
Carton2:	250mm(L)×200mm(W)×80mm (H)	× 2	1	2
Carton3:	320mm(L)×270mm(W)×80mm (H)	nein-	-	-
Carton4:	435mm(L)×335mm(W)×290mm(H)	-	-	-
Carton5:	600mm(L)×430mm(W)×290mm(H)	1	30	30

Disclaimer: The product design is subject to alternation and improvement without prior notice.



# **5 Debugging tools**

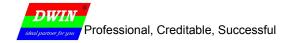
It is recommended for new users of DWIN smart LCMs to purchase official accessories. For more details, please refer to customer service center.



#### 6 T5L series IC features

- (1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle) high speed operation.
- (2) Separate GUI CPU Core running DGUS II System
- High-speed display memory, 2.4GB/S bandwidth.
- 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280\*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
- Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
- Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
- way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
- 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
- Support development by DGUS V7.624 and simulation on PC. Support background remote upgrade.
- (3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:
- Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
- 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
- 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channel 16-bit PWM of adjustable resolution.
- Support IAP on-line simulation and debugging with unlimited number of breakpoints.
- Upgrade code online through DGUS system.
- (4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- (5) Operating temperature ranges from -40  $^{\circ}$ C to +85  $^{\circ}$ C (IC operating temperature customizable from -55  $^{\circ}$ C to 105  $^{\circ}$ C).

DWIN encourages users to design your own customized product based on T5L.



#### 7 Revision records

Rev	Revise Date	Content	Editor
00	2020-05-09	First Edition	ZK
01	2021-11-05	Upgrade version	ZYJ
02	2021-12-31	Update flash size, physical drawing and RTC accuracy description	ZYJ
03	2022-05-31	Update installation Dimensions and format	FKX

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Thank you all for continuous support of DWIN, and your approval is the driving force of our

progress!