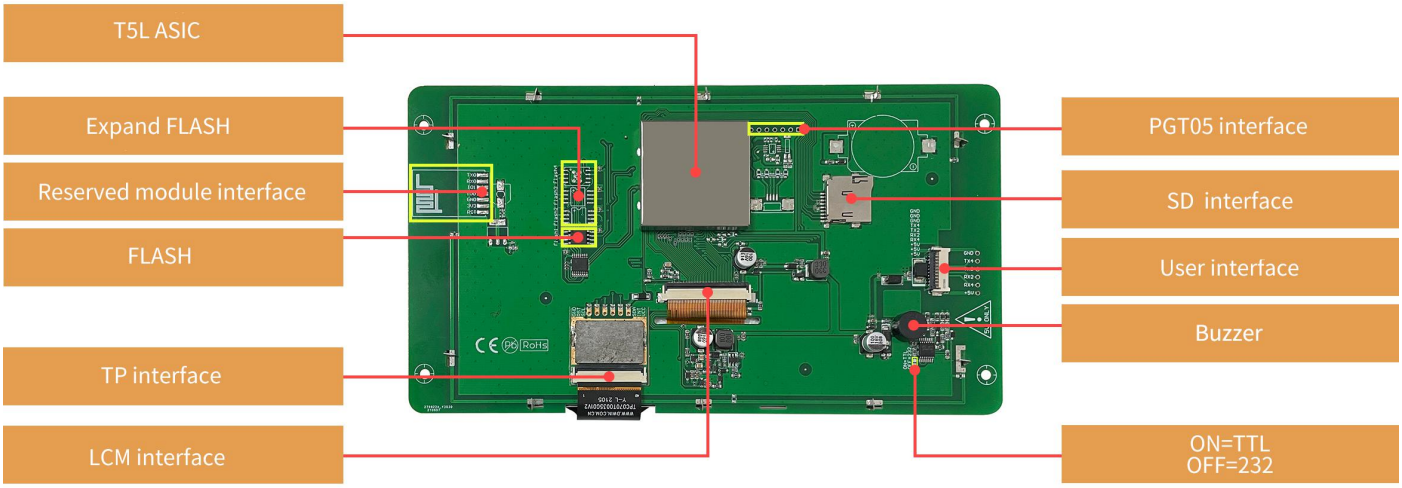
DMG10600C070_03WTC

Features:

- **Based on T5L2, running DGUS II system, commercial grade.**
- **7.0-inch, 1024*600 pixels resolution, 16.7M colors, IPS-TFT-LCD, wide viewing angle.**
- **Capacitive touch screen.**

1.Hardware and interface

1.1 Hardware interface



Hardware interface

1.2 Interface description

No.	Name	Description
1	T5L2 ASIC	Developed by DWIN in 2019. 1MBytes Nor Flash on the chip, 512Kbytes storage for the user database. Rewrite cycle > 100,000 times
2	LCM interface	FPC50_0.5mm, RGB interface
3	CTP interface	COB structure, IIC interface
4	User interface	10Pin_1.0mm socket for power supply and serial communication. Download rate(typical value): 12KByte/s
5	Flash	16MBytes NOR Flash, for fonts, pictures and audio files. Rewrite cycle: over 100,000 times
6	Expand Flash	Expandable to 48Mbytes NOR Flash or 32Mbytes NOR Flash + 512Mbytes NAND Flash
7	Buzzer	3V passive buzzer. Power: <1W
8	SD card slot	The SD card should be formatted as FAT32 file system. Download files by SD interface can be displayed in statistics. Download rate: 4Mb/s
9	Reserved module interface	Wi-Fi module: connect to the cloud platform to update remotely USB module: download files by USB flash disk
10	PGT05 interface	When product crashes by accident, you can use PGT05 to update DGUS kernel and make the product return to normal

2.Specification parameters

2.1 Display parameters

LCD Type	IPS, TFT LCD
Viewing Angle	Wide viewing angle, 85°/85°/85°/85°（L/R/U/D）
Resolution	1024×600 pixels (support 0°/90°/180°/270°)
Color	24-bit 8R8G8B
Active Area (A.A.)	154.20mm (W)×85.90mm (H)
Visual Area (V.A.)	-
Backlight	LED
Backlight Lifetime	>20000 hours (Time of the brightness decaying to 50% on the condition of continuous working with the maximum brightness)
Brightness	270nit
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)
Note: Long time display of high contrast still image over 30 minutes may lead to display residual shadow, please use screen saver to avoid this problem.	

2.2 Touch parameters

Type	CTP (Capacitive touch panel)
Structure	G+G structure with surface cover of Asahi tempered glass
Touch Mode	Support point touch and drag
Surface Hardness	6H
Light Transmittance	Over 90%
Life	Over 1,000,000 times touch

2.3 Serial interface parameters

Mode	UART2: ON=TTL/CMOS; OFF=RS232 UART4: ON=TTL/CMOS; OFF=RS232(Only available after OS configuration)				
Voltage Level	Test Condition	Min	Typ	Max	Unit
	Output 1, Iout = 1mA	3.0	3.3	-	V
	Output 0, Iout = -1mA	-	0	0.3	V
	Input 1, Iin = 1mA	2.4	3.3	5.0	V
	Input 0, Iin = -1mA	0	-	0.5	V
Baud Rate	3150~3225600bps, typical value of 115200bps				
Data Format	UART2: N81 UART4: N81/E81/O81/N82 4 modes (OS configuration)				
Interface Cable	10Pin_1.0mm				

2.4 Electrical specifications

Rated Power	<5W	
Operating Voltage	4.5~5.5V, typical value of 5V	
Operating Current	500mA	VCC = +5V, Backlight on
	120mA	VCC = +5V, Backlight off
Recommended power supply: 5V 1A DC		

2.5 Operating environment

Operating Temperature	-20℃~70℃ (5V @ 60% RH)
Storage Temperature	-30℃~80℃
Conformal coating	None
Operating Humidity	10%~90%RH, typical value of 60% RH

3.Reliability test

3.1 Electrostatic discharge test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and air discharge in turn of the serial screen iron frame and display area as shown in Fig.3.1 below. During the experimental process, it was observed whether the screen is dead, black, white, splash, or reboot. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.

3.1 Electrostatic discharge test

Discharge Type	Discharge Value	Result
Contact discharge	±4KV	Normal operation
Air discharge	±4KV	Normal operation

3.2 EFT test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and the smart screen is energized by the power supply coupled with a EFT generator as shown in Fig. 3.2 below. During the experimental process, it was observed whether abnormal reset, display or touch phenomena occurs. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.

3.2 EFT test

Test Item	Test Standard	Result
Power supply	±1KV;100KHz	Normal operation

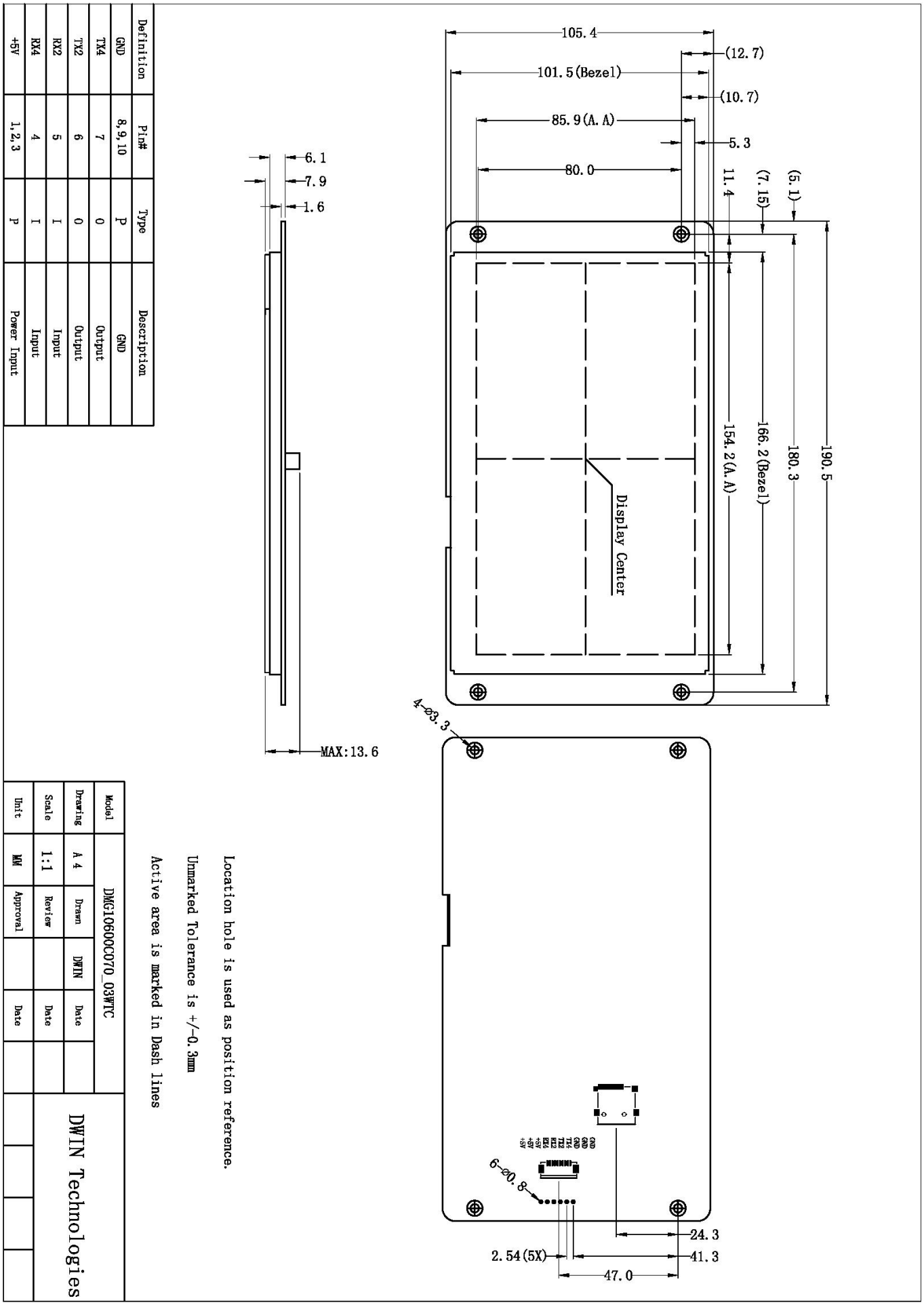
4.Packaging & dimensions

Form Factor	190.50mm (W)×105.40mm (H)×13.60mm (T)
Installation Dimensions	Positioning hole: 166.20 (+0.3mm)×101.50 (+0.3mm)
Net Weight	260g
Packaging Standards	

Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)
Carton1：	220mm(L)×160mm(W)×47mm (H)	1	1	1
Carton2：	250mm(L)×200mm(W)×80mm (H)	2	1	2
Carton3：	320mm(L)×270mm(W)×80mm (H)	2	2	4
Carton4：	435mm(L)×335mm(W)×290mm(H)	-	-	-
Carton5：	600mm(L)×430mm(W)×290mm(H)	1	40	40

Disclaimer: the data is for reference only and the information of product design that do not affect performance parameters and utilization is subject to alternation without prior notice.

Definition	Pin#	Type	Description
GND	8,9,10	P	GND
TX4	7	O	Output
TX2	6	O	Output
RX2	5	I	Input
RX4	4	I	Input
+5V	1,2,3	P	Power Input



Location hole is used as position reference.

Unmarked Tolerance is +/-0.3mm

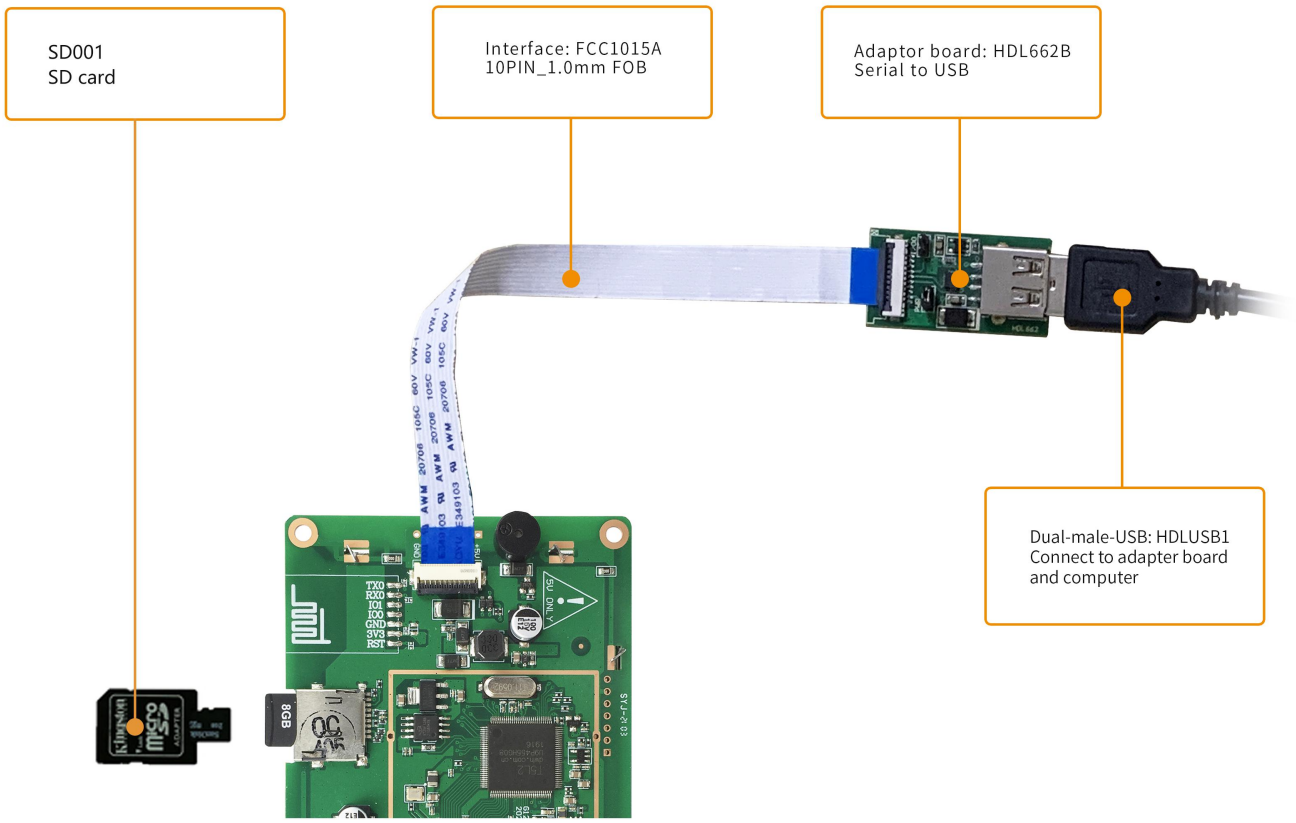
Active area is marked in Dash lines

Model	DMG10600C070_03WTC			
Drawing	A 4	Drawn	DWIN	Date
Scale	1:1	Review		Date
Unit	MM	Approval		Date

DWIN Technologies

5.Debugging tools

It is recommended for new users of DWIN smart LCMs to purchase official accessories. For more details, please refer to customer service center.



6. T5L series IC features

(1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle) high speed operation.

(2) Separate GUI CPU Core running DGUS II System

- High-speed display memory, 2.4GB/S bandwidth.
- 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
- Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
- Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
- way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
- 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
- Support development by DGUS V7.624 and simulation on PC. Support background remote upgrade.

(3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:

- Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
- 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
- 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channle 16-bit PWM of adjustable resolution.
- Support IAP on-line simulation and debugging with unlimited number of breakpoints.
- Upgrade code online through DGUS system.

(4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.

(5) Operating temperature ranges from -40℃ to +85℃(IC operating temperature customizable from -55℃ to 105℃).

DWIN encourages users to design your own customized product based on T5L.

7. Revision records

Rev	Revise Date	Content	Editor
00	2019-03-26	First Edition	ZK
01	2019-08-22	Modify Interface	ZK
02	2019-11-26	Modify Interface Description	ZK
03	2020-03-07	Change bezel	ZK
04	2020-09-18	Update pictures	ZK
05	2020-11-24	Update CAD drawing	ZK
06	2021-08-02	Update pictures（Add flash pad and RTC line、COB structure）	ZYJ
07	2021-11-04	Upgrade version	ZYJ

Please contact us if you have any questions about the use of this document or our products, or if you would like to know the latest information about our products.

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!