



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Summary

BVDSS	R _{DS(ON)} Max	I _D Max T _A = +25°C
60V	85mΩ @ V _{GS} = 10V	2.5A
607	120mΩ @ $V_{GS} = 4.5V$	2.0A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- DC-DC converters
- Power-management functions
- Backlighting

Features and Benefits

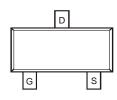
- N MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/
- An automotive-compliant part is available under a separate datasheet (<u>DMN6075SQ</u>)

Mechanical Data

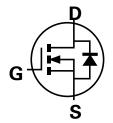
- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)







Top View



Top View Pin Configuration

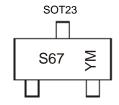
Ordering Information (Note 4)

Orderable Part Number	rderable Part Number Package Reel Size (inches)		Tana Width (mm)	Packing	
Orderable Part Number	Package	Reel Size (Iliches)	Tape Width (mm)	Qty.	Carrier
DMN6075S-7	SOT23	7	8	3,000	Tape & Reel
DMN6075S-13	SOT23	13	8	10,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



S67 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: M = 2025) M = Month (ex: 9 = September)

Date Code Key

Year	2014	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	В	-	М	N	Р	R	S	Т	U	V	W	Χ
	_					_		_				_
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lo	2.0 1.5	А
Continuous Drain Current (Note 6) $V_{GS} = 10V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		lo	2.5 2.0	А	
Maximum Body Diode Forward Current (Note 5)			Is	2.0	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6)		I _{DM}	12	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Dawer Dissipation (Note 5)	$T_A = +25$ °C	D-	0.8	W	
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.5		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	157	°C/W	
Total Power Dissipation (Note 6)	T _A = +25°C	Б	1.15	W	
Total Fower Dissipation (Note 6)	T _A = +70°C	P_D	0.7		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	110	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

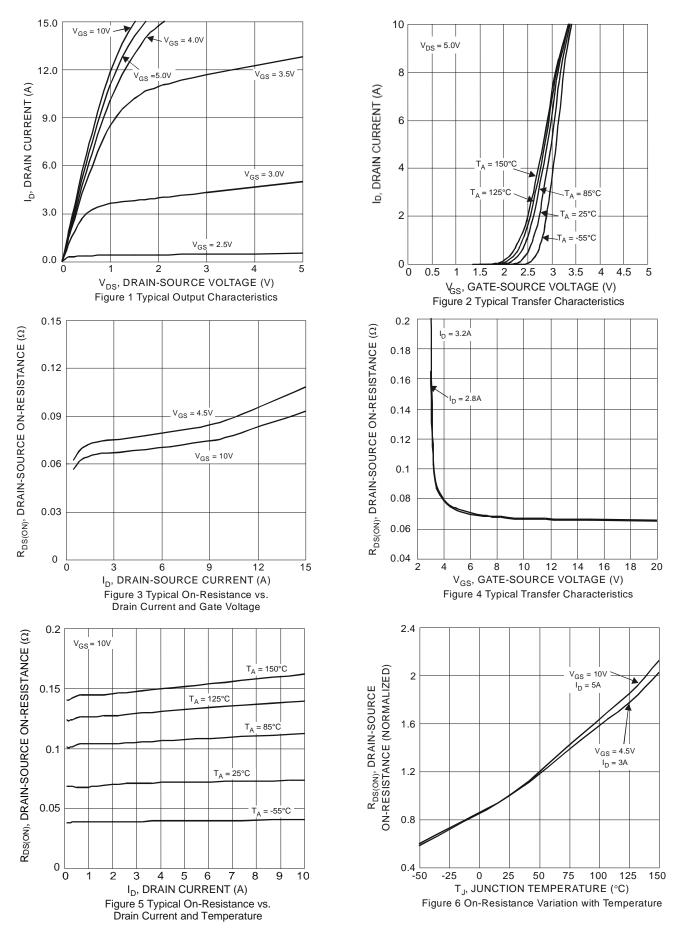
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BVDSS	60			V	V _{GS} = 0, I _D = 250μA		
Zero Gate Voltage Drain Current, T _J = +25°C	IDSS	_	1	1.0	μA	V _{DS} = 60V, V _{GS} = 0		
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0$		
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _G S(TH)	1.3	ı	2.7	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$		
Static Drain-Source On-Resistance	Descour		69	85	mΩ	Vgs = 10V, ID = 3.2A		
Static Dialif-Source Off-Resistance	RDS(ON)	_	75	120	mtz	Vgs = 4.5V, ID = 2.8A		
Diode Forward Voltage	V _{SD}	_	1	1.2	٧	$V_{GS} = 0$, $I_S = 2.5A$		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C _{iss}	_	606		pF	V 99V V 9		
Output Capacitance	Coss	_	32.6		pF	$V_{DS} = 20V, V_{GS} = 0,$ f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	24.6	_	pF	1 = 1.000112		
Gate Resistance	Rg	_	1.5	_	Ω	V _{DS} = 0, V _{GS} = 0, f = 1MHz		
Total Gate Charge (V _{GS} = 10V)	Qg	_	12.3	_	nC			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.6	_	nC	V 20V I 2A		
Gate-Source Charge	Qgs	_	1.7	_	nC	$V_{DS} = 30V, I_{D} = 3A$		
Gate-Drain Charge	Q_{gd}	_	1.9	_	nC			
Turn-On Delay Time	tD(ON)	_	3.5		ns			
Turn-On Rise Time	t _R	_	4.1	_	ns	V _G S = 10V, V _D S = 30V,		
Turn-Off Delay Time	t _{D(OFF)}	_	35	_	ns	$R_g = 20\Omega$, $R_L = 50\Omega$		
Turn-Off Fall Time	tF	_	11	_	ns			

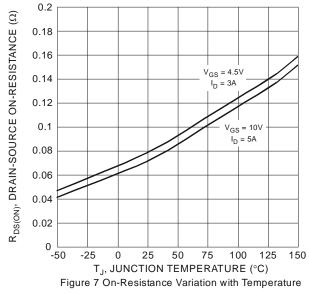
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

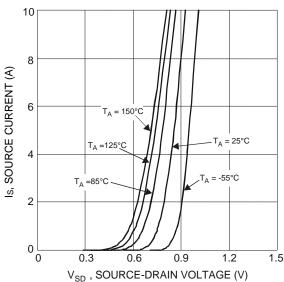
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.
 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.

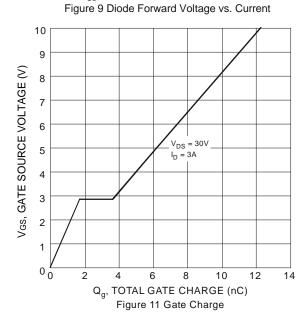












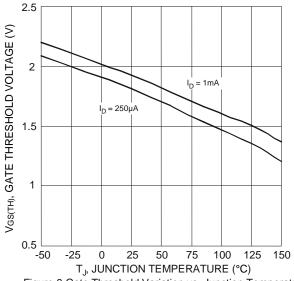
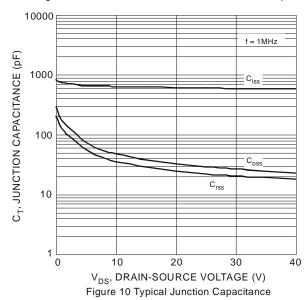
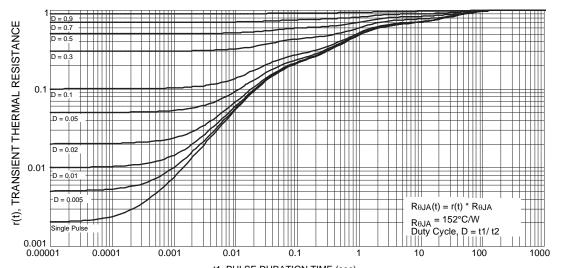


Figure 8 Gate Threshold Variation vs. Junction Temperature



100
R_{DS(ON)}
10
10
10
R_{DS(ON)}
10
10
10
10
10
10
10
100
V_{DS}, DRAIN-SOURCE VOLTAGE (V)
Figure 12 SOA, Safe Operation Area





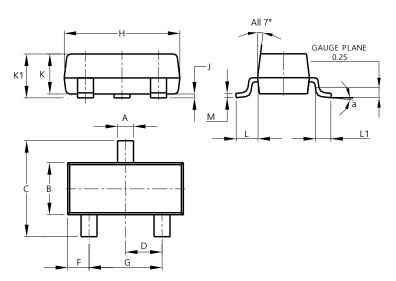
t1, PULSE DURATION TIME (sec) Figure 13 Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

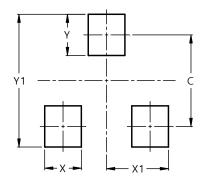


	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
M	0.085	0.150	0.110					
а	0°	8°						
All	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Y	0.9
Y1	29



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